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oplicant(s): HARADA et al.

Application No.: 10/814,804

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Title: MULTI-LAYER PRINTED CIRCUIT

BOARD AND METHOD FOR MANUFACTURING THE SAME Group Art Unit: 2841

Examiner: PATEL, ISHWARBHAI B

Attorney Docket No.: 01-628

Allowed: October 05, 2005 Confirmation No.: 5306

Commissioner for Patents Alexandria, VA 22314 Mail Stop Issue Fee

December 22, 2005

ISSUE FEE TRANSMITTAL LETTER

Sir:

Enclosed are one original and one copy of an executed Issue Fee Transmittal Form PTOL-85B for the above allowed patent application as well as a check in the amount of \$1700.00 for payment of the requisite issue fee and publication fee.

Authorization is hereby given to charge any fee deficiencies or credit any overpayment to Deposit Account 50-1147.

Respectfully submitted,

David G. Posz

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